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Application : <u>10/653237</u>	Examiner : <u>Wilczewski</u>	GAU : <u>2822</u>
From: <u>DUP</u>	Location: IDC <u>FME</u> FDC	Date: <u>5/23/2005</u>

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/653,237	09/03/2003	Kazuo Matsuoka	20433-00601-US1	4842

TITLE OF INVENTION: SEMICONDUCTOR DEVICE INCLUDING TRANSISTOR WITH COMPOSITE GATE STRUCTURE AND TRANSISTOR WITH SINGLE GATE STRUCTURE, AND METHOD FOR MANUFACTURING THE SAME

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	03/23/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
WILCZEWSKI, MARY A	2822	438-257000			

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27,369

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